Proceedings of the Annual General Donald R. Keith Memorial Conference West Point, New York, USA April 28, 2016 A Regional Conference of the Society for Industrial and Systems Engineering

Simulation of High-Mix Low-Volume Circuit Card Assembly Process

Jordi Escorcia¹, Dylan Gagnon¹, Rachel Laghezza¹, Matthew Montes¹, Abhishek Paul¹, Thomas J. Schaefer¹ and Anthony Paradiso²

¹Binghamton University

²BAE Systems

Corresponding author's Email: apaul10@binghamton.edu

Author Note: As a Capstone Senior Design Project team in the Industrial and Systems Engineering department at Binghamton University, we would like to extend our thanks to the project coordinators at BAE Systems in Endicott, NY for assigning us this project and assisting us in the development of the simulation.

Abstract: The Printed Circuit Assembly (PCA) area at BAE Systems is defined as a high-mix low-volume shop and is experiencing an increase in volume of new products, staffing reductions, and inefficient product flows. To reduce the risk of late deliveries, our team was tasked with creating a simulation using Process Simulator, by ProModel, software to develop a thorough understanding of the process in order to locate bottlenecks, create recommendations to remove them, and develop a strategy for increasing efficiency in the product flow. At the moment we have defined the system and created a process flow map. We are now conducting an analysis of Work In Process (WIP) data and developing the current state simulation. The WIP data analysis and simulation outputs will be compared to determine locations of bottlenecks to target for process improvement.

Keywords: Printed Circuit Assembly, Work In Process, Simulation